-	19	s e\$6 adj memory adj display adj da	USPAT;	2004/05/28
			US-PGPUB; EPO; JPO;	15:58
			DERWENT;	
-	4	store\$6 adj memory adj display adj data	IBM_TDB USPAT;	2004/05/28
		and frame\$1	US-PGPUB;	15:39
			EPO; JPO;	
			DERWENT; IBM TDB	
-	358	store \$6 adj memory and display adj data and memory adj cell \$1	USPAT;	2004/05/28
		and memory adj cerrs:	US-PGPUB; EPO; JPO;	15:59
			DERWENT;	
_	346	store\$6 adj memory and display adj data	IBM_TDB USPAT;	2004/05/28
		and memory adj cell\$1 and read\$9	US-PGPUB;	15:59
			EPO; JPO; DERWENT;	
			IBM TDB	
-	184	store\$6 adj memory and display adj data and memory adj cell\$1 and read\$9 adj	USPAT;	2004/05/28
•		memory	US-PGPUB; EPO; JPO;	15:59
			DERWENT;	
_	174	store\$6 adj memory and display adj data	IBM_TDB USPAT;	2004/05/28
		and memory adj cell\$1 and read\$9 adj	US-PGPUB;	16:00
		memory and different	EPO; JPO; DERWENT;	
	4	stoness and management and a second	IBM_TDB	
	, a	store\$6 adj memory and display adj data and memory adj cell\$1 and read\$9 adj	USPAT; US-PGPUB;	2004/05/28 16:00
		memory and different adj order	EPO; JPO;	10.00
			DERWENT; IBM TDB	
-	4	store\$6 adj memory and display adj data	USPAT;	2004/05/28
		and memory adj cell\$1 and read\$9 adj memory and different adj order	US-PGPUB; EPO; JPO;	16:00
		1	DERWENT;	
-	0	memory adj read adj different adj order	IBM_TDB USPAT;	2004/05/30
		and every adj frame\$1 and display	1 ' 1	11:07
	0	memory adj read adj different adj order		2004/05/30
-	0	memory adj cell adj read adj different	USPAT;	11:07 2004/05/30
_	0	adj order memory adj cell adj read and different	EPO; JPO	11:08
]	adj order and every adj frame	i I	2004/05/30 11:08
	0	memory adj cell adj read and different adj order and every adj frame\$1		2004/05/30
_	1	memory adj cell adj read and different		11:08 2004/05/30
_	0	adj order and every and frame\$1 memory adj cell and read adj different	EPO; JPO	11:09
		adj order and every and frame\$1		2004/05/30 11:09
-	14	memory adj cell adj read and different adj order	USPAT;	2004/05/30
_	2	memory adj cell adj read and different		11:09 2004/05/30
_	2	adj order and display 6,043,818.pn.	EPO; JPO	11:09
		0,015,010.pm.	1	2004/05/30 11:11
1		j	EPO; JPO;	
			DERWENT; IBM TDB	
_	0	6,043,818.pn. and read adj different adj	_	2004/05/30
		order	US-PGPUB; EPO; JPO;	11:12
			DERWENT;	
L			IBM TDB	

-	0	and read and different ad	USPAT;	2004/05/30
		order	US-PGPUB;	11:12
			EPO; JPO;	1
	1		DERWENT;	
_	0	6 043 919 pp and different 11	IBM_TDB	İ
		6,043,818.pn. and different adj order	USPAT;	2004/05/30
			US-PGPUB;	11:12
			EPO; JPO;	
			DERWENT;	
_	0	6,043,818.pn. and read adj different and	IBM_TDB USPAT;	2004/05/30
		order .	US-PGPUB;	11:12
1			EPO; JPO;	11.12
			DERWENT;	
			IBM TDB	
-	1	, , it is a sound asset asset asset asset	USPAT;	2004/05/30
		order	US-PGPUB;	11:13
			EPO; JPO;	
			DERWENT;	
_		44-44-3 - 34 1 3 31	IBM_TDB	1
		digital adj signal adj peocessor	USPAT;	2004/05/30
			US-PGPUB;	11:41
			EPO; JPO;	
	1		DERWENT;	
_	47002	digital adj signal adj processor	IBM_TDB	2004/05/20
		and ording and brocepant	USPAT; US-PGPUB;	2004/05/30
	ł		EPO; JPO;	11:41
			DERWENT;	
			IBM TDB	
-	0	l andreas are l professor and	USPAT;	2004/05/30
		memory adj read adj different adj order	US-PGPUB;	11:42
		and every adj frame\$1 and display	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	2	i j aaj biginal aaj piocessoi and	USPAT;	2004/05/30
		memory adj read and different adj order	US-PGPUB;	11:43
	-	and every adj frame\$1 and display	EPO; JPO;	
			DERWENT;	
_	2	digital adj signal adj processor and	<pre>IBM_TDB USPAT;</pre>	2004/05/30
		memory adj read and different adj order	US-PGPUB;	11:44
		and every adj frame\$1 and display and	EPO; JPO;	11.44
]	image\$1	DERWENT;	
			IBM TDB	
_	0	digital adj signal adj processor and	USPAT;	2004/05/30
	į i	memory adj read and different adj order	US-PGPUB;	11:44
	ļ	adj address\$1 and every adj frame\$1 and	EPO; JPO;	
		display and image\$1	DERWENT;	
_	ol	digital add gigmal add games	IBM_TDB	
		digital adj signal adj processor and	USPAT;	2004/05/30
	,	memory adj read and different adj order adj address\$6 and every adj frame\$1 and	US-PGPUB;	11:44
		display and image\$1	EPO; JPO;	
		are brain and imagest	DERWENT;	
_	o	digital adj signal adj processor and	IBM_TDB USPAT;	2004/05/20
		memory adj read and different adj order	US-PGPUB;	2004/05/30
		adj address\$9 and every adj frame\$1 and	EPO; JPO;	11.70
		display and image\$1	DERWENT;	
		-	IBM TDB	
-	0	digital adj signal adj processor and	USPAT;	2004/05/30
	1	different adj order adj address\$9 and	US-PGPUB;	11:45
		every adj frame\$1 and display and image\$1	EPO; JPO;	
		· · · · · · · · · · · · · · · · · · ·	DERWENT;	
			IBM_TDB	
-	29	digital adj signal adj processor and	USPAT;	2004/05/30
	İ	different adj address\$9 and every adj	US-PGPUB;	11:45
	· I	frame\$1 and display and image\$1	EPO; JPO;	
			DERWENT; IBM TDB	

Search History 5/30/04 4:35:55 PM Page 6

-	0	digi adj signal adj processor	and	USPAT;	2004/05/30
		different adj address\$9 and oredr	and	US-PGPUB;	11:45
		every adj frame\$1 and display and	image\$1	EPO; JPO;	
				DERWENT;	
				IBM TDB	
-	29	y= au processor	and	USPAT;	2004/05/30
		different adj address\$9 and order	and	US-PGPUB;	11:46
		every adj frame\$1 and display and	image\$1	EPO; JPO;	
			•	DERWENT;	
				IBM TDB	
-	28190633	digital adj signal adj processor	and	USPAT;	2004/05/30
		memory adj read and different adj		US-PGPUB;	11:46
		address\$9 and order and every adj	frame\$1	EPO; JPO;	
ĺ		and display and image\$1 a		DERWENT;	
		-		IBM TDB	
-	0		and	USPAT:	2004/05/30
		memory adj read and different adj		US-PGPUB;	11:47
		address\$9 and order and every adj	frame\$1	EPO; JPO;	
		and display and image\$1		DERWENT;	
		•		IBM TDB	l
_	29		and	USPAT;	2004/05/30
		memory and read and different adj		US-PGPUB;	11:47
1		address\$9 and order and every adj	frame\$1	EPO; JPO;	
		and display and image\$1	, ,	DERWENT;	į
				IBM TDB	